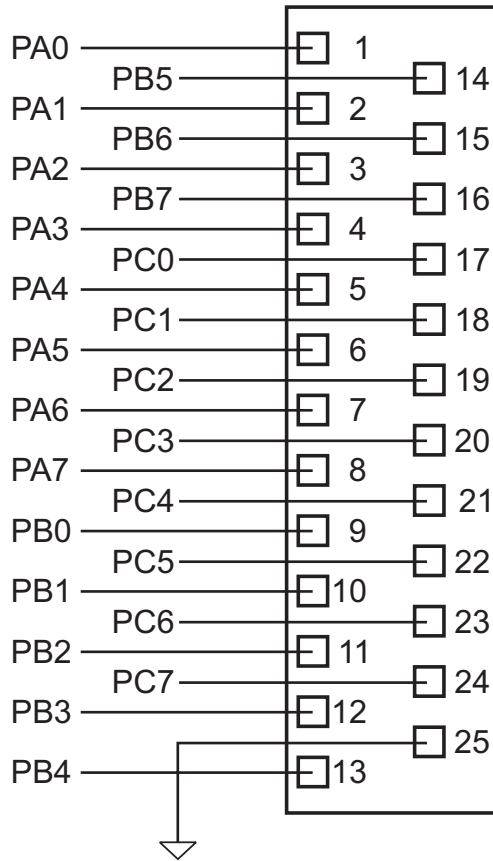


Lava 8255PIO Interface Board Connector Pinouts

Intel 82C55A
pin symbols

DB25S

Notes:



PA₃₋₀ Port A, (82C55A Pins 0-3): Lower nibble of an 8-bit data output latch/buffer and an 8-bit data input latch.

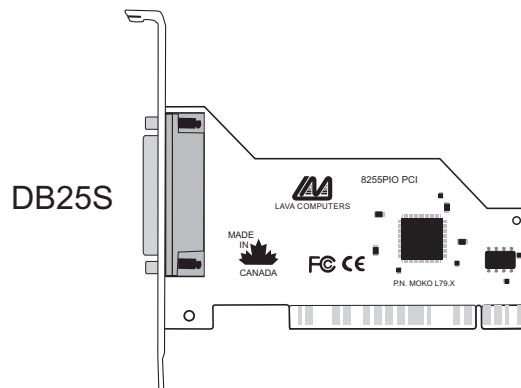
PA₇₋₄ Port A, (82C55A Pins 4-7): Upper nibble of an 8-bit data output latch/buffer and an 8-bit data input latch.

PB₀₋₇ Port B, (82C55A Pins 0-7): An 8-bit data output latch/buffer and an 8-bit data input buffer.

PC₀₋₃ Port C, (82C55A Pins 0-3): Lower nibble of Port C.

PC₇₋₄ Port C, (82C55A Pins 4-7): Upper nibble of an 8-bit data output latch/buffer and an 8-bit data input buffer (no latch for input). This port can be divided into two 4-bit ports under the mode control as specified by Intel. Each 4-bit port contains a 4-bit latch and it can be used for the control signal outputs and status signal inputs in conjunction with ports A and B.

Details on the modes and operation of the Intel 82C55A CMOS Programmable Peripheral Interface can be found in the Intel 82C55A specification sheet, Intel order number 231256-004.



Lava: "The Source For Ports"



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