

LAVA 8255-PIO

Programmable Peripheral Interface (PPI) Board

PCI Bus Versatile Programmable Interface Board

Features & Benefits

- Adds three 8-bit TTL compatible I/O ports to PCI-equipped PCs
- I/O ports provide up to 24 lines of fully programmable digital I/O (1 bit per line)
- Enables interfacing any device to a PC
- Easily programmable in assembler, QBasic, Turbo C, C, etc.
- Compliant with PCI Specification 2.1
- Lava Lifetime Warranty

Connectors

- One DB-25

Address (HEX/DEC)

218-21F (536-543)
250-277 (592-631)
280-2EF (640-751)
300-31F (768-799)

Description

Available
Available
Available
Available

Address (HEX/DEC)

390-39F (906-927)
3AA-3AF (938-943)
3B0-3BF (944-959)
3F8-3FF

Description

Available
Available
Available
COM 1

System Requirements

- PCI host slot

Drivers ship with product. For updated software support visit www.lavalink.com.



FC CE

